

REMARKS

Claims 1-20 are pending and under consideration. Claims 1, 10, and 17 are amended herein. Support for the amendments to the claims may be found in the claims as originally filed and at page 10, lines 17-22, page 11, lines 1-13, and Figs. 5A and 5B of the specification. This amendment is believed to place the application in condition for allowance, and entry therefore is respectfully requested. In the alternative, entry of this amendment is requested as placing the application in better condition for appeal by, at least, reducing the number of issues outstanding. Further reconsideration is requested based on the following remarks.

Response to Arguments:

The Applicants appreciate the consideration given to their arguments. The Applicants, however, are disappointed that their arguments were not found to be persuasive. Further reconsideration is thus requested.

REJECTION UNDER 35 U.S.C. § 102:

Claims 1-8, 11-17, and 19 were rejected under 35 U.S.C. § 102(b) as anticipated by Fleck *et al.*, US 6,128,641 (hereinafter “Fleck”). The rejection is traversed to the extent it might apply to the claims as amended. Reconsideration of the rejection is respectfully requested.

Claim 1 recites:

The task switching function executing a return operation to the third data processing task.

Fleck neither teaches, discloses, nor suggests “the task switching function executing a return operation to the third data processing task,” as recited in claim 1. Claim 1 is thus submitted to be allowable. Withdrawal of the rejection of claim 1 is earnestly solicited.

Claims 2-8 depend from claim 1 and add further distinguishing elements. Claims 2-8 are thus also submitted to be allowable. Withdrawal of the rejection of claims 2-8 is earnestly solicited.

Claims 11-16:

Claim 11 recites:

The memory management apparatus responsive to said instruction information indicating a return instruction for moving said return address from said second storage location to a register of the data processor.

Fleck neither teaches, discloses, nor suggests "the memory management apparatus responsive to said instruction information indicating a return instruction for moving said return address from said second storage location to a register of the data processor," as recited in claim 11. Claim 11 is thus submitted to be allowable. Withdrawal of the rejection of claim 11 is earnestly solicited.

Claims 12-16 depend from claim 11 and add further distinguishing elements. Claims 12-16 are thus also submitted to be allowable. Withdrawal of the rejection of claims 12-16 is earnestly solicited.

Claim 17:

Claim 17 recites:

The task switcher switching from execution of the second task to execution of a third task, said memory having a storage location for storing a return address corresponding to the third task, and an input for receiving information indicative of instructions of a task switching function that has been called by the second task.

Fleck neither teaches, discloses, nor suggests "the task switcher switching from execution of the second task to execution of a third task, said memory having a storage location for storing a return address corresponding to the third task, and an input for receiving information indicative of instructions of a task switching function that has been called by the second task," as recited in claim 17. Claim 17 is thus submitted to be allowable. Withdrawal of the rejection of claim 17 is earnestly solicited.

REJECTION UNDER 35 U.S.C. § 103:

Claims 10 and 18 were rejected under 35 U.S.C. § 103(a) as unpatentable over Fleck. Claims 10 and 18 depend from claims 1 and 17, respectively and add further distinguishing elements. Fleck neither teaches, discloses, nor suggests "the task switching function executing a return operation to the third data processing task," as discussed above with respect to the rejections of claim 1. Fleck neither teaches, discloses, nor suggests "the task switcher switching from execution of the second task to execution of a third task, said memory having a storage location for storing a return address corresponding to the third task, and an input for receiving information indicative of instructions of a task switching function that has been called by the second task," as discussed above with respect to the rejection of claim 17. Claims 10 and 18 are thus submitted to be allowable. Withdrawal of the rejection of claims 10 and 18 is earnestly solicited.

Claims 9 and 20:

Claims 9 and 20 were rejected under 35 U.S.C. § 103(a) as unpatentable over Fleck in view of "Applicant's Admitted Prior Art." Claims 9 and 20 depend from claims 1 and 17, respectively and add further distinguishing elements. Fleck neither teaches, discloses, nor suggests "the task switching function executing a return operation to the third data processing task," as discussed above with respect to the rejections of claim 1. Fleck neither teaches, discloses, nor suggests "the task switcher switching from execution of the second task to execution of a third task, said memory having a storage location for storing a return address corresponding to the third task, and an input for receiving information indicative of instructions of a task switching function that has been called by the second task," as discussed above with respect to the rejection of claim 17. The parts of the Application labeled "Prior Art" do not either, and thus cannot make up for the deficiencies of Fleck with respect to claims 9 and 20. Claims 9 and 20 are thus submitted to be allowable. Withdrawal of the rejection of claims 9 and 20 is earnestly solicited.

Conclusion:

Claims 1-20 are submitted to be allowable over the cited references. There being no further outstanding objections or rejections, it is submitted that the application is in condition for allowance. An early action to that effect is courteously solicited.

If there are any formal matters remaining after this response, the Examiner is requested to telephone the undersigned to attend to these matters.

If there are any additional fees associated with filing of this Amendment, please charge the same to our Deposit Account No. 19-3935.

Respectfully submitted,

STAAS & HALSEY LLP

By:

Thomas E. McKiernan
Registration No. 37,889

Date: 09MY06

1201 New York Ave, N.W., 7th Floor
Washington, D.C. 20005
Telephone: (202) 434-1500
Facsimile: (202) 434-1501